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| Project Name | **Sirius** | ECO # | SIR-265 |
| Version No. | **Rev. A** |  |  |
| Requesting Engineer | HUIBO ZHONG | Request Date | 20170905 |
| Approve Manager | Song Pan | Request Date |  |
| Problem to be fixed | 1. **Bug/Issue description:**   If input video is stable for more than 2 frame, video will store to ddr by default setting, so firmware need disable this function if this is not needed.   1. **Impact if this ECO is not implemented:**   Vif2isp data path will error | | |
| Affected Parts | 1. **Affected IP(s)**   Video\_IF   1. **Affected Partition(s)**   SUB\_1   1. **Current status of affected Partitions(s)  (Placement, Pre-CTS, Post-CTS, Post-Route, STA, etc)**   **STA** | | |
| Fix Solution | **Describe briefly how to solve the problem.**   1. .ADD NEW LOGIC TO GENERATE THE FULL SIGNAL | | |
| Change Description | 1. **Change method (manual ECO fix on netlist or partial Re-synthesis):**   Manual ECO fix @SUB 1   1. **Change effort :** 2. **Connection change or Logic fix:** 3. **How many DFFs or combinational cells will be added or affected in this ECO?**   ADD 16 LOGIC GATE     1. **Does it have interface change on IP top level? If yes, please describe it in detail**      1. **Change Schematics:**      1. **Others (for analog macros):** | | |
| IP interface change |  | | |
| File to be changed | video\_if\_reg.v  encell.v | | |
| Change Impacts | 1. **Impacts on scan chain :**   NO.   1. **Impacts on timing critical path:**   NO.   1. **Impacts on IP size and/or floorplan:**   NO. | | |
| Verification status | Done. | | |